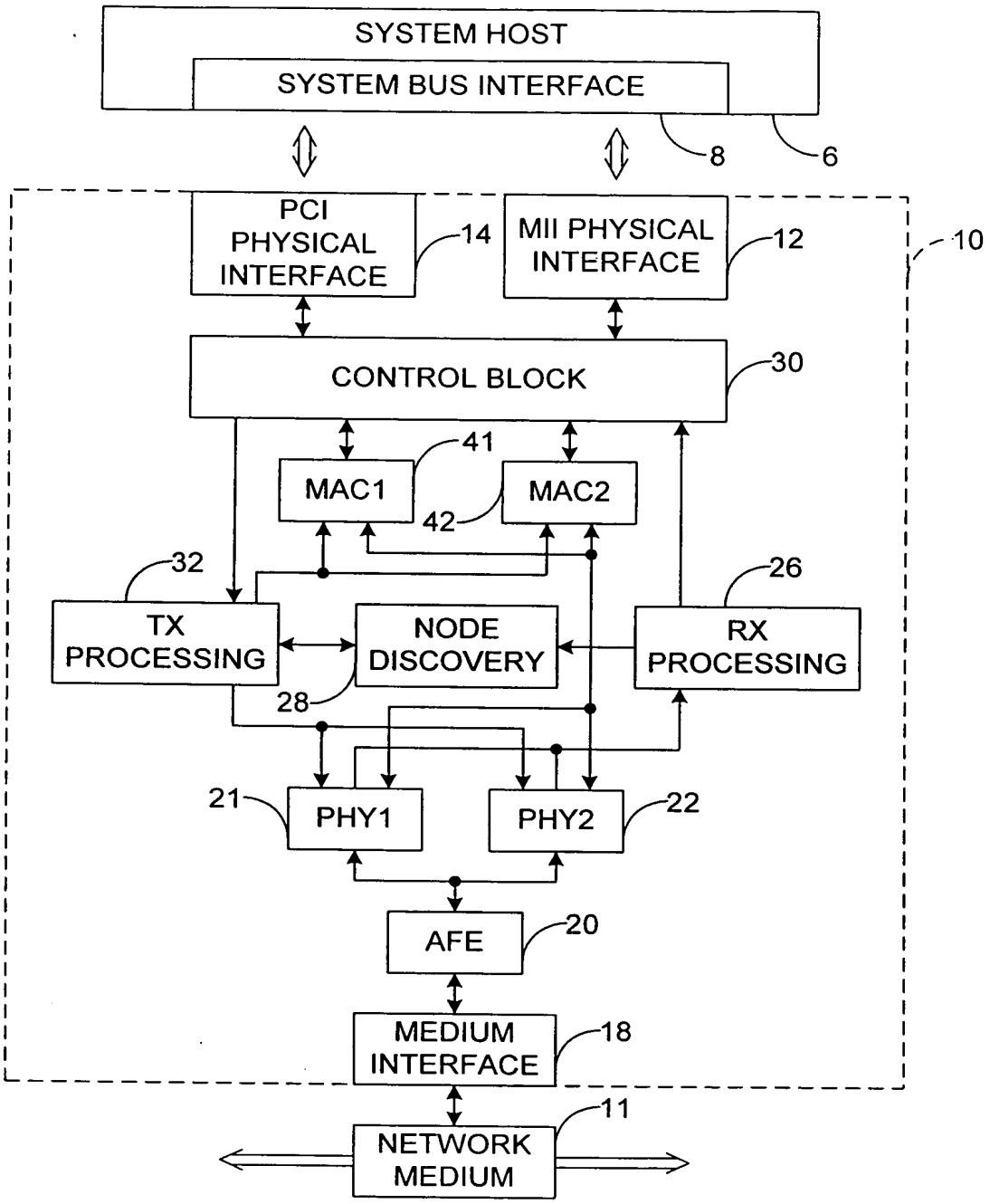

FIG. 1

09769800.012501

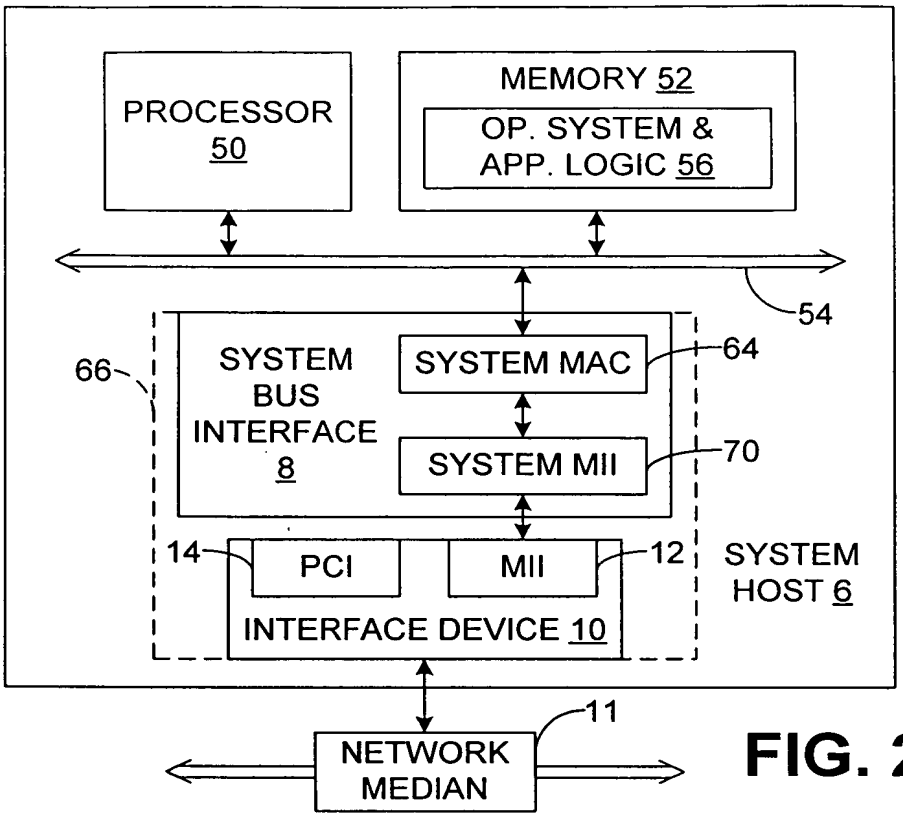
PROCESSOR
50
MEMORY 52
OP. SYSTEM &
APP. LOGIC 56
54
66
SYSTEM
BUS
INTERFACE
8
SYSTEM MAC
64
SYSTEM MII
70
14
PCI
MII
12
INTERFACE DEVICE 10
SYSTEM
HOST 6
11
NETWORK
MEDIAN

FIG. 2

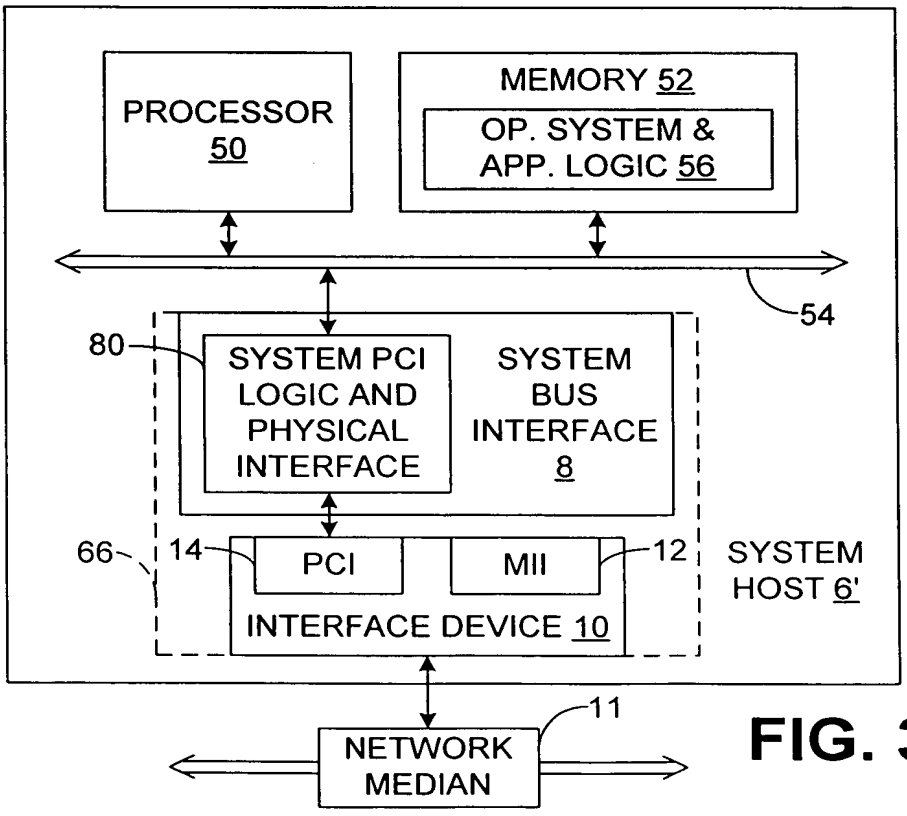
PROCESSOR
50
MEMORY 52
OP. SYSTEM &
APP. LOGIC 56
54
80
SYSTEM PCI
LOGIC AND
PHYSICAL
INTERFACE
SYSTEM
BUS
INTERFACE
8
66
14
PCI
MII
12
INTERFACE DEVICE 10
SYSTEM
HOST 6'
11
NETWORK
MEDIAN

FIG. 3

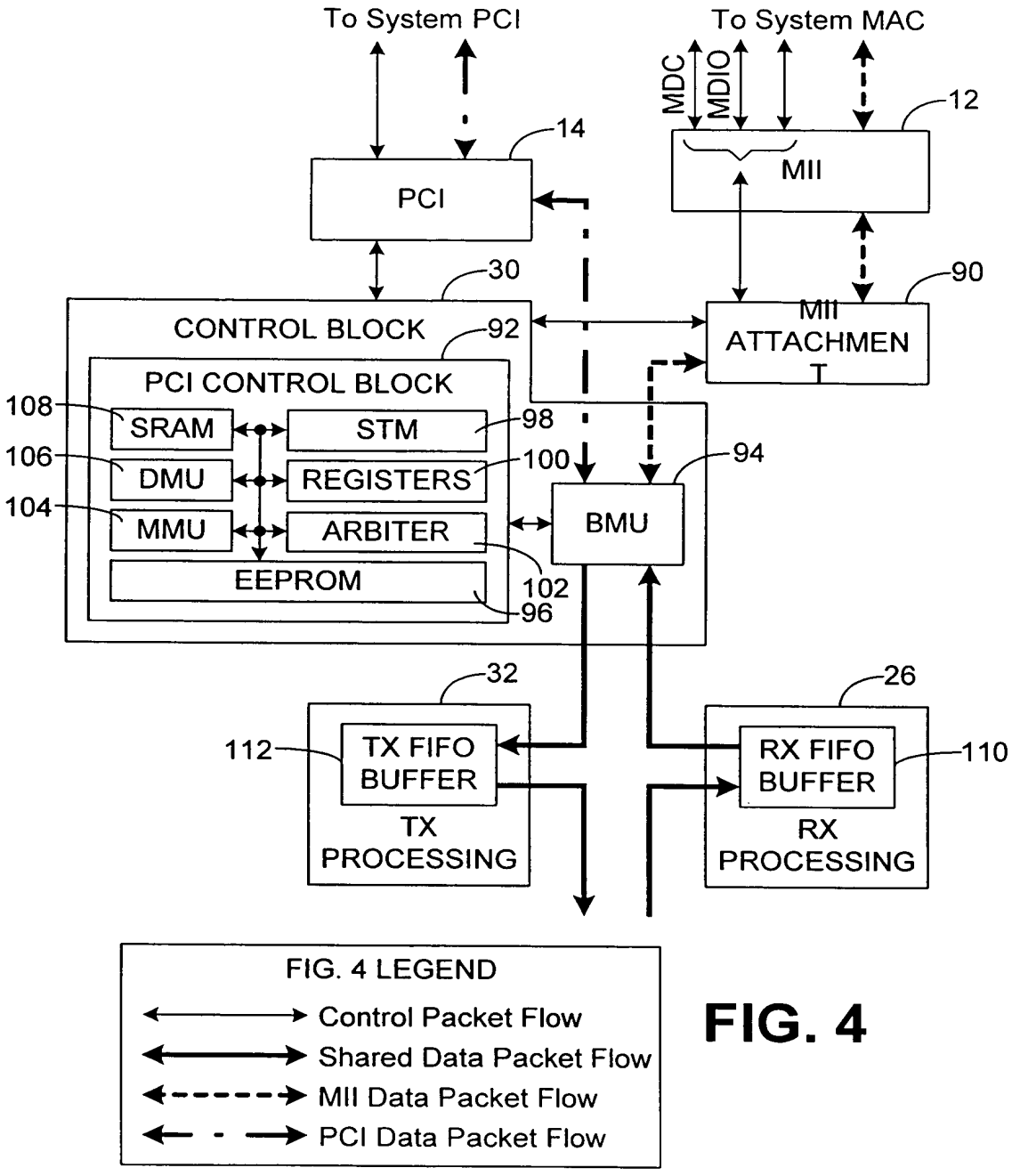

FIG. 4

10525210 "052504

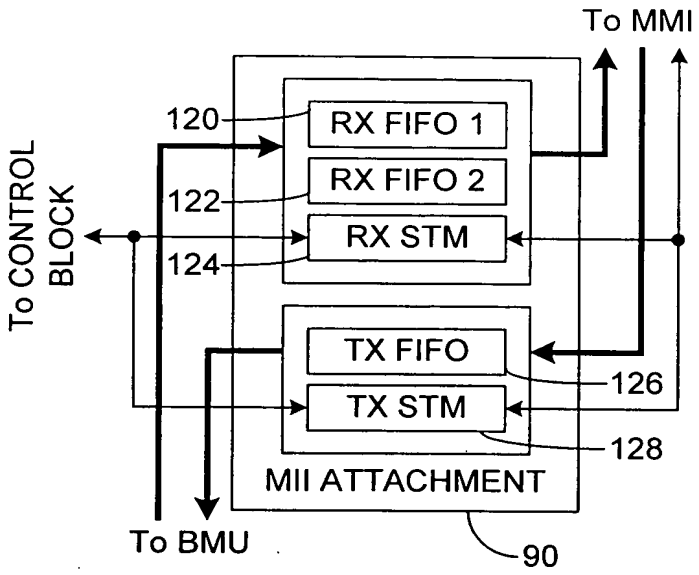

FIG. 5

FIG. 5 LEGEND

Control Packet Flow

Data Packet Flow

10525101 - 050696